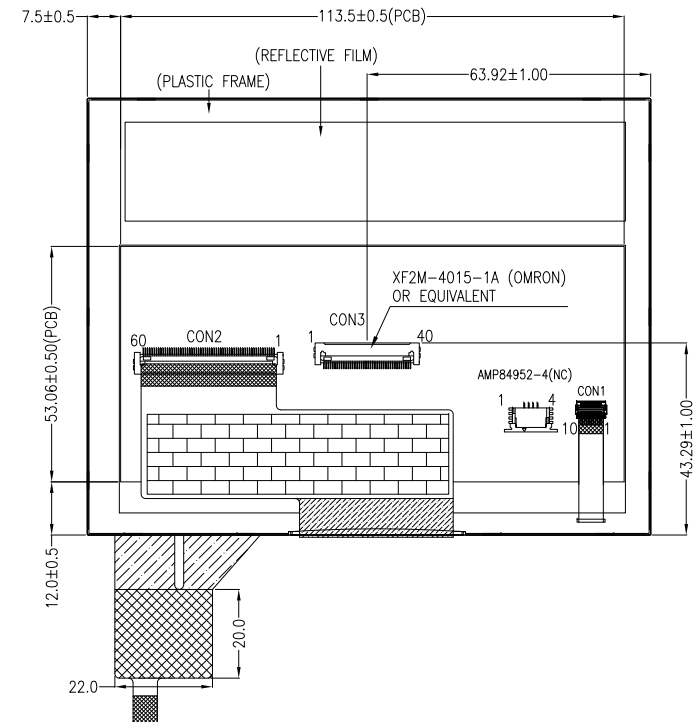
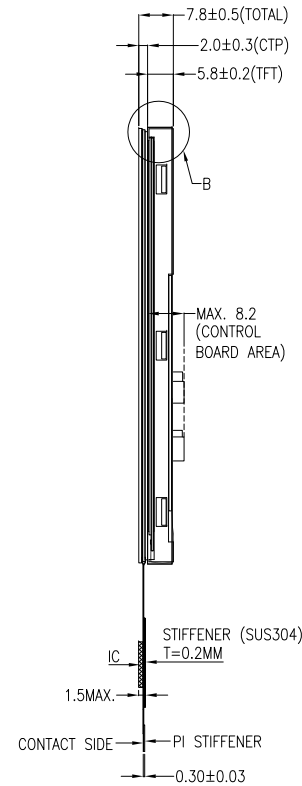
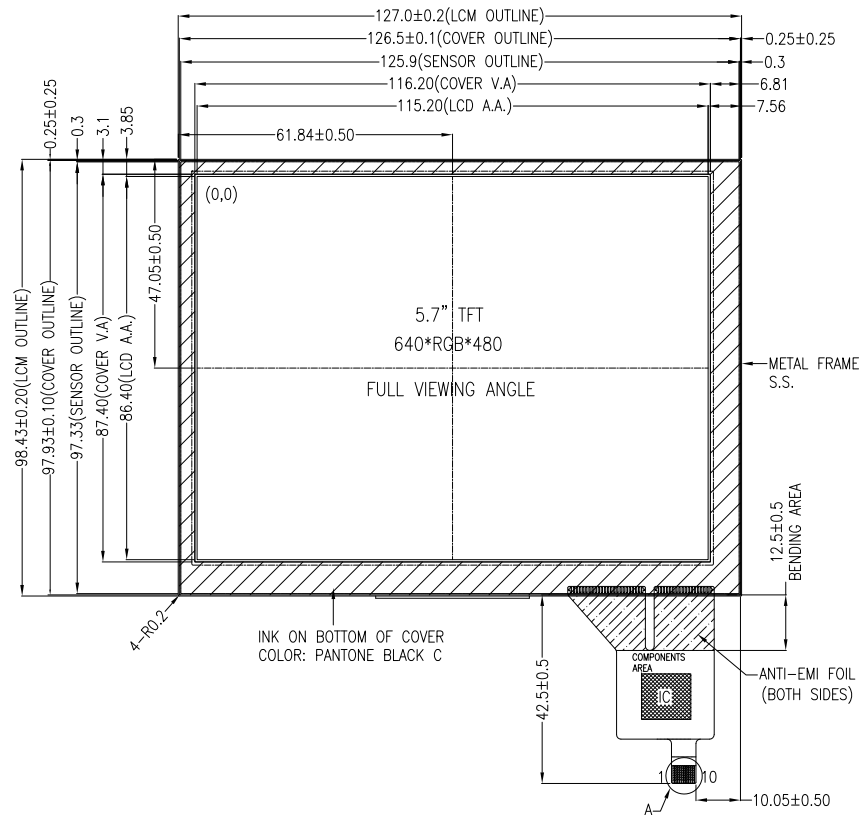


NO MECHANICAL HOUSING TO HOLD THE TFT, TOUCH PANEL AND PCBA ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT, TOUCH PANEL AND THE PCBA.



PIN	DESCRIPTION FOR CON3
PIN	SYMBOL
1	U/D
2	NC
3	HSYNC
4	VLED
5	VLED
6	VLED
7	VCC
8	VSYN
9	DE
10	NC(XL)
11	NC(YU)
12	ADJ
13	B5
14	B4
15	B3
16	VSS
17	B2
18	B1
19	B0
20	VSS
21	G5
22	G4
23	G3
24	VSS
25	G2
26	G1
27	G0
28	VSS
29	R5
30	R4
31	R3
32	VSS
33	R2
34	R1
35	R0
36	NC(XR)
37	NC(YD)
38	DLCL
39	VSS
40	L/R

PIN DESCRIPTION FOR CTP	
PIN	SYMBOL
1	GND
2	VDD
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	GND

NOTES FOR TFT:

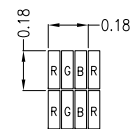
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCC=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: JD9168S
5. LED BACKLIGHT: WHITE LED
BUILT-IN BACKLIGHT DRIVER: VLED=5.0V (TYP.)
6. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE

NOTES FOR OTHERS:

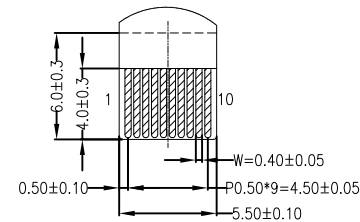
1. OPERATING TEMP.: -30°C ~ 80°C
2. STORAGE TEMP.: -30°C ~ 80°C
3. SURFACE LUMINANCE: 850 CD/M² (TYP.)
4. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
5. GENERAL TOLERANCE: ±0.3MM
6. ROHS COMPLIANT

NOTES FOR CTP:

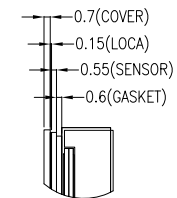
1. 5.7 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: VDD=IOVDD=3.3V
4. DRIVER IC: FT7311DQQ
5. INTERFACE: IIC
6. RESOLUTION: 640*480
7. MULTI FINGER: UP TO 5
8. HARDNESS OF COVER SURFACE: 7H
9. FIRMWARE FILE: TBD



PIXEL DETAIL
(SCALE 50:1)



DETAIL A
(SCALE 4:1)



DETAIL B
(SCALE 2:1)



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	CAROL	2024.05.08		MODULE P/N:	
CHECKED BY:			SCALE : 1/1		M0570AET-3CP
APPROVED BY:			UNIT: mm	DESCRIPTION:	
VERSION NO:	01		SHEET NO.: 1/1		TFT + CTP MODULE